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It also includes an object browser for viewing documentation. The Manager's open debugging and ... Currently supported simulators include ModelSim from Model Technology, Leapfrog, Verilog-XL and ...

~~Summit Design Launches Visual IP Version 3.0 for PC Platform Support~~
The checker is supported with Incisive NCVERILOG, VCS and Modelsim simulators ... Ocpperf2 is used to measure Bandwidth, Latency, etc. SOLV documentation can be referred to for the available options ...

by Maq Mannan President and CEO, DSM Technologies Chairman of the IEEE 1364 Verilog Standards Group Past Chairman of Open Verilog International One of the major strengths of the Verilog language is the Programming Language Interface (PLI), which allows users and Verilog application developers to infinitely extend the capabilities of the Verilog language and the Verilog simulator. In fact, the overwhelming success of the Verilog language can be partly attributed to the exi- ence of its PLI. Using the PLI, add-on products, such as graphical waveform displays or pre and post simulation analysis tools, can be easily developed. These products can then be used with any Verilog simulator that supports the Verilog PLI. This ability to create thi- party add-on products for Verilog simulators has created new markets and provided the Verilog user base with multiple sources of software tools. Hardware design engineers can, and should, use the Verilog PLI to customize their Verilog simulation environment. A Company that designs graphics chips, for ex- ple, may wish to see the simulation results of a new design in some custom graphical display. The Verilog PLI makes it possible, and even trivial, to integrate custom so- ware, such as a graphical display program, into a Verilog simulator. The simulation results can then dynamically be displayed in the custom format during simulation. And, if the company uses Verilog simulators from multiple simulator vendors, this integrated graphical display will work with all the simulators.

Conference proceedings of the U.S. Tcl conference of 2012

The objective of this document is to support the simulation results reported by Corbin et al. (2005) by documenting the requirements, conceptual model, simulation methodology, testing, and quality assurance associated with the Hanford Soil Inventory Model (SIM). There is no conventional software life-cycle documentation associated with the Hanford SIM because of the research and development nature of the project. Because of the extensive use of commercial- off-the-shelf software products, there was little actual software development as part of this application. This document is meant to provide historical context and technical support of Corbin et al. (2005), which is a significant revision and update to an earlier product Simpson et al. (2001). The SIM application computed waste discharges composed of 75 analytes at 377 waste sites (liquid disposal, unplanned releases, and tank farm leaks) over an operational period of approximately 50 years. The development and application of SIM was an effort to develop a probabilistic approach to estimate comprehensive, mass balanced-based contaminant inventories for the Hanford Site post-closure setting. A computer model capable of calculating inventories and the associated uncertainties as a function of time was identified to address the needs of the Remediation and Closure Science (RCS) Project.

This handbook gives comprehensive coverage of all kinds of industrial control systems to help engineers and researchers correctly and efficiently implement their projects. It is an indispensable guide and references for anyone involved in control, automation, computer networks and robotics in industry and academia alike. Whether you are part of the manufacturing sector, large-scale infrastructure systems, or processing technologies, this book is the key to learning and implementing real time and distributed control applications. It covers working at the device and machine level as well as the wider environments of plant and enterprise. It includes information on sensors and actuators; computer hardware; system interfaces; digital controllers that perform programs and protocols; the embedded applications software; data communications in distributed control systems; and the system routines that make control systems more user-friendly and safe to operate. This handbook is a single source reference in an industry with highly disparate information from myriad sources. * Helps engineers and researchers correctly and efficiently implement their projects. * An indispensable guide and references for anyone involved in control, automation, computer networks and robotics. * Equally suitable for industry and academia

Intended for a course on VHDL Programming for 6th/7th Semester students of CSE, IT, and ECE, this text can also be promoted for Digital Systems Design courses having a portion on VHDL. The text follows a bottom-up approach, whereby Language Fundamentals have been discussed followed by various styles of Description and Design Examples.

FPGA Prototyping Using Verilog Examples will provide you with a hands-on introduction to Verilog synthesis and FPGA programming through a “ learn by doing ” approach. By following the clear, easy-to-understand templates for code development and the numerous practical examples, you can quickly develop and simulate a sophisticated digital circuit, realize it on a prototyping device, and verify the operation of its physical implementation. This introductory text that will provide you with a solid foundation, instill confidence with rigorous examples for complex systems and prepare you for future development tasks.